Parallel SAT Solving - Using More Cores

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SAT solving is widely applied, however

- most solvers are sequential
- architecture becomes parallel
- a good scalable parallelization is still missing

How to parallelize the solving process?

Modern SAT solver are based on DPLL with many improvements.

- Conflict analysis
 - Undo more decisions than only the last one (usually)
 - The variable order on the current path changes often
- Restarts
- Learned clause removal
- Advanced algorithms
 - Conflict resolution is linear (15%)
 - Two-Watched-Literal unit propagation (80%)

- Run the same solver multiple times
 - sharing learned clauses and more
- Split the search
 - by splitting the search tree and solving subformulas
 - by splitting the formula and finding multiple models per subformula
- Run additional tasks in separat threads (e.g. autarky detection)

Problems:

- Higher memory bandwidth / more memory accesses
- Splitting might not lead to easier subformulas

Why not parallelize the solver itself?

Runtime properties of an exemplary SAT solver

(all measurements are based on riss and the SAT09 industrial benchmark, 1h timeout)

- Unit propagation needs 80 %
- Propagating a literal reveals more implied literals
 - $\bullet\,$ at least 2 more in 13 % of the cases
 - $\bullet\,$ at least 4 more in 4 % of the cases

Suggestion:

Parallelize UP by splitting the formula, share implied literals found in subformulas

Separate formula into n partitions

- Use function Assign: clause \rightarrow partition
- Currently: alternating
- Oreate n threads that execute UP in parallel
 - create assignment, trail, propagation queue per thread
 - choose a master thread
 - initialize structures with according clause partition
- Use Assign to distribute learned clauses
- Oo backjumping for all threads
- Semove learned clauses in all threads
 - Can result in unbalanced load

```
0:traditionalPropagate(){
```

- 1: C = 0;
- 2: while(not myQueue.processed()){
- 3: 1 = myQueue.dequeue(); // keep on queue
- 4: C = propagate(1); // enqueues implied literals
- 5: if(C != 0) break;
- 6: }
- 7: return C;
- 8:}

Sequential propagation uses its private propagation queue, assignment and trail.

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Master thread executes CDCL algorithm and wakes slave threads for UP

```
0:propagate(){
1: while( (not all finished) and (no conflict signaled)){
2:
     C = traditionalPropagate();
3:
     if( C != 0 ){ signalConflict( C ); break; }
4: check all other threads for new literals;
5: }
6: if( conflict not from master ){
7: updateMaster();
8: }
9:}
```

Reading other threads data is lock- and waitfree.

Given scenario: Formula with 5 clauses, 2 threads

 ${\sf F}=\langle [
eg 1,2],[
eg 1,4],[
eg 2,3],[
eg 2,5],[
eg 4,
eg 5]
angle$

Splitted formula: $T_1: [\neg 1, 2], [\neg 2, 3], [\neg 4, \neg 5]$ $T_2: [\neg 1, 4], [\neg 2, 5]$

Algorithm execution:

| T_1 : | Reason | - | <i>C</i> ₁ | <i>C</i> ₃ | | <i>C</i> ₂ | <i>C</i> ₅ | | | | | | | <i>C</i> ₄ |
|-------------------------|--------|---|-----------------------|-----------------------|---|-----------------------|-----------------------|---|-----------------------|-----------------------|-------|-----------------------|---|-----------------------|
| | Queue | 1 | 2 | 3 | S | 4 | _5 | | | | | | S | 5 |
| | Step | 0 | 1 | | 2 | 3 | 4 | 5 | 6 | | | 7 | 8 | 9 |
| <i>T</i> ₂ : | Queue | 1 | 4 | | | | | S | 2 | 3 | _5 | 5 | | |
| | Reason | - | <i>C</i> ₂ | | | | | | <i>C</i> ₁ | <i>C</i> ₃ | C_5 | <i>C</i> ₄ | | |

 T_2 signals conflict in step 7

 T_1 performs update in step 8 and 9

Propagation results in conflict

Conditions: 100 instances, 15 minutes timeout

| Configuration | Seq1 | D1 | D2 | D3 |
|------------------|---------|---------|---------|---------|
| Solved instances | 48 | 61 | 64 | 68 |
| Average runtime | 191.721 | 219.941 | 193.019 | 215.212 |

Speedup on the 41 commonly solved instances

| Average | Maximal | D1 | Median |
|---------|---------|------|--------|
| 1.091 | 1.578 | 1.28 | 1.3 |

Runtime distribution on a single instance



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Runtime distribution on a single instance



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Problems:

- waiting times (4 % idle, 1.5 % system)
- no load balancing
- scales not beyond 2 threads

Possible Solutions:

- find a well working Assign function
- introduce load balancing to reduce idle times
- use spin locks instead of the sleep-state
- combine presented approach with existing methods

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- Parallel UP is possible
- Not many additional memory accesses are needed
- Speedup is not yet optimal
- Further analysis has to be done
- Presented approach can be combined with existing solutions
- Used numer of cores can be doubled

Thanks

The solver is available at https://gitorious.org/riss

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